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APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

FIRST SEMESTER B.TECH DEGREE EXAMINATION, APRIL 2018

Course Code: BE101-04

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OR

- 14 a) Draw and explain the circuit of a common emitter RC coupled amplifier using (6) NPN transistorwith voltage divider biasing.
 - b) Draw the frequency response of a RC coupled amplifier and explain how gain (4) reduces at low and high frequencies.

Module IV

- 15 a) Draw the V_{DS} v/s I_D curve of an enhancement MOSFET and mark different operating regions. (3)
 - b) With help of neat sketches, explain how an increase in V_{DS} affects channel field and drain current in a MOSFET. (7)

OR

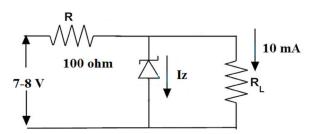
- 16 a) Draw the structure of N channel depletion MOSFET. (3)
 - b) Explain the working of a depletion mode MOSFET. (7)

Module V

- 17 a) Draw the circuit of a full wave bridge rectifier and derive the equations for V_{rms} , V_{dc} and ripple factor. (7)
 - b) Explain the term rectifier efficiency. (3)

OR

- 18 a) Draw the block diagram of a DC power supply and explain the working of each stage. (5)
 - b) Find the minimum and maximum current flowing through the Zener diodeas shown (5) in figure for a regulated output of 5 V. Choose proper value for R_L.



Module VI

- 19 a) Draw the block diagram and explain the working of a CRO. (6)
 - b) Explain with help of diagrams, how phase difference between two signals can be measured using CRO. (4)

OR

- 20 a) Draw the block diagram and explain the working of a digital storage oscilloscope. (6)
 - b) List and describe the various types of measurement errors. (4)
